

Amendments to the Specification:

Please replace the title of the invention with the following:

--MICROPROCESSOR FOR PROCESSING VARIOUS ASSEMBLER CODES AND A
METHOD OF RELATIVE ADDRESSING IN A MICROPROCESSOR--

Please **add** the following paragraph to the Brief Description of
the Drawings, **after** the paragraph ending on page 9, line 8:

--Figs. 4A and 4B are partial block circuit diagrams for
adding and subtracting, respectively, an offset value from an
instruction length.--

Please replace the paragraphs beginning on page 12, line 13
and extending to line 24, with the following paragraphs:

--According to the invention, for example, when using the
address of the current instruction line in the instruction
counter of the microprocessor, the instruction length can then
optionally be added to the offset value, as shown in Fig. 4A,
if using an assembler that specifies that the instruction
counter must point to the next assembler instruction.

Similarly, when managing the address of the next assembler
instruction in the instruction counter of the processor, the
instruction length can be subtracted from the offset value, as
shown in Fig. 4B, if an assembler for which the instruction
counter must always point to the current assembler instruction
is to be processed.--